Re: **Student** (Student ID **xxxxxx**)

Program: Master of Science in Electrical Engineering

To whom it may concern:

I serve as the Graduate Advisor in Electrical Engineering at San José State University. **Student** graduated with a Master’s of Science in Electrical Engineering in **Month** of **Year**. As the Graduate Advisor, I have detailed knowledge of the department’s courses. I have reviewed transcripts and, based on my knowledge, I confirm the acquisition of hands-on background in the following skills through semester-long coursework in the pertinent courses described below: ***[List own skills and classes as examples below]***

1. Skill: Computer Architecture

Course: EE 275 - Adv Comp Architect

1. Skill: Digital Logic Design

Course: EE 271 - Digital Sys Design

1. Skill: ASIC design, development and/or validation

Course: EE287 - ASIC CMOS Design

1. Skill: Design Verification

Course: EE 272 - SOC Design

1. Skill: RTL design, coding, simulation, verification, and/or place/route

Course: EE 271 - Digital Sys Design

1. Skill: System Debug

Course: EE287 - ASIC CMOS Design

Sincerely,

Dr. Hiu-Yung Wong

Graduate Advisor

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